			
	Application No.	Applicant(s)	
Notice of Allowability	09/988,183	LEE ET AL.	
	Examiner	Art Unit	
	Nathan W. Ha	2814	
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGOT THE Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in this apport of the communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. \boxtimes This communication is responsive to $8/2/04$.			
2. The allowed claim(s) is/are <u>1 and 5-15</u> .			
3. \boxtimes The drawings filed on <u>17 September 2004</u> are accepted by	the Examiner.		
4.			
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐	5. Notice of Informal F 6. Interview Summary Paper No./Mail Da 7. Examiner's Amendr 8. Examiner's Stateme 9. Other	(PTO-413), te ment/Comment	

DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1 and 5-15 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

After carefully reviewed the Applicant's arguments and newly amended claims in paper filed 9/17/04, and updated prior art of record, the Examiner finds that the Applicant's arguments and newly added limitations are reasonable and strongly persuasive. Therefore, they place the application in condition for allowance. The primary reason for the indication of the allowability of the above claims is the inclusion therein, in combination as currently claimed, of the limitation of the use of the method of making a dual work function gate electrode of a CMOS including a step laser annealing the CMOS for a period of time between 40ns and 80ns. This limitation is found in the above claims and is neither disclosed nor taught by the prior art of record, alone or in combination.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nathan W. Ha whose telephone number is (571) 272-1707. The examiner can normally be reached on M-TH 8:00-7:00(EST).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Nathan Ha
October 16, 2004

LONG PHAM PRIMARY EXAMINER